

ISMVL 2016

May 18 – 20, 2016, Sapporo, Japan

Call for Papers



The IEEE Computer Society's Technical Committee on Multiple-Valued Logic will hold its 46th annual symposium in Sapporo, Japan, in May 2016. You are invited to submit an original paper, survey, or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

Algebra and Formal Aspects
Automatic Reasoning
Logic Programming
Philosophical Aspects of MVL
Fuzzy Logic and Soft Computing
Data Mining
Machine Learning and Robotics
Quantum Computing
Reversible Computing

Logic Design and Switching Theory
Test and Verification
Spectral Techniques
Circuit/Device Implementation
VLSI Architecture and Computing
System-on-Chip Technology
MVL Aspects of Nanotechnology
MVL Aspects of Medical Technologies
MVL Approaches to Big Data

Authors should submit papers in PDF format on the conference website by ~~November 2, 2015~~ **November 30, 2015 (extended)**. Each manuscript should include a 50-to-100 word abstract, and should not exceed 6 pages in the 2-column IEEE Proceedings format. Special issues of some journals are planned for selected ISMVL 2016 papers. Authors will be notified about acceptance by **February 1, 2016**. Camera-ready copies of accepted papers are due on **March 1, 2016**. For submission details, please visit the symposium website: <http://mvl.jp.org/ISMVL2016/>

Travel Support for Students:

We plan to secure some money to support students to attend ISMVL 2016. If the first author of the paper is a student and the student is going to present the paper at ISMVL, then he/she is eligible to receive a travel grant. When submitting a paper, please indicate whether the first author is a student.

The symposium will be co-located with the 25th International Workshop on Post-Binary ULSI Systems. Information on accommodations and transportation to the Sapporo Region will be provided at a later date. This event is sponsored by the IEEE Computer Society and the Japan MVL research group, and technically supported by the Graduate School of Information Science and Technology, Hokkaido University. For additional information, please contact:

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