The IEEE Computer Society's Technical Committee on Multiple-Valued Logic will hold its 47th annual symposium in Novi Sad, Serbia, in May 2017. You are invited to submit an original paper, survey, or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

- Algebra and Formal Aspects
- Automatic Reasoning
- Logic Programming
- Philosophical Aspects of MVL
- Fuzzy Logic and Soft Computing
- Data Mining
- Machine Learning and Robotics
- Quantum Computing
- Reversible Computing
- Logic Design and Switching Theory
- Test and Verification
- Spectral Techniques
- Circuit/Device Implementation
- VLSI Architecture and Computing
- System-on-Chip Technology
- MVL Aspects of Nanotechnology
- MVL Aspects of Medical Technologies
- MVL Approaches to Big Data

Authors should submit papers in PDF format on the conference website by **November 30, 2016**. Each manuscript should include a 50-to-100 word abstract, and should not exceed 6 pages in the 2-column IEEE Proceedings format. Special issues in several journals (Journal of Multiple-Valued Logic and Soft Computing and Elsevier Journal of Applied Logic) are planned for selected ISMVL 2017 papers.

Authors will be notified about acceptance by **February 1, 2017**. Camera-ready copies of accepted papers are due on **March 1, 2017**. For submission details, please visit the symposium website:

http://mvl.jpn.org/ISMVL2017/

The symposium committee is also planning to hold a special subsymposium tribute to Ivan Stojmenović.

The symposium will be co-located with the 26th International Workshop on Post-Binary ULSI Systems and Reed-Muller 2017 Workshop. Information on accommodations and transportation to the Novi Sad will be provided at a later date. This event is sponsored by the IEEE Computer Society and the University of Novi Sad. For additional information, please contact:

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