

# ISMVL 2019

May 21 – 23, 2019, Fredericton, New Brunswick, Canada

## Call for Papers



Source: Fredericton Tourism

The IEEE Computer Society's Technical Committee on Multiple-Valued Logic will hold its 49<sup>th</sup> annual symposium in Fredericton, Canada, in May 2019. You are invited to submit an original paper, survey, or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

*Algebra and Formal Aspects*  
*Automatic Reasoning*  
*Logic Programming*  
*Philosophical Aspects of MVL*  
*Fuzzy Logic and Soft Computing*  
*Data Mining*  
*Machine Learning and Robotics*  
*Quantum Computing*  
*Reversible Computing*

*Logic Design and Switching Theory*  
*Test and Verification*  
*Spectral Techniques*  
*Circuit/Device Implementation*  
*VLSI Architecture and Computing*  
*System-on-Chip Technology*  
*MVL Aspects of Nanotechnology*  
*MVL Aspects of Medical Technologies*  
*MVL Approaches to Big Data*

Authors should submit papers in PDF format on the conference website by **November 4, 2018**. Each manuscript should include a 50-to-100 word abstract, and should not exceed 6 pages in the 2-column IEEE Proceedings format. Special issues of some journals are planned for selected ISMVL 2019 papers.

Authors will be notified about acceptance by **February 3, 2019**. Camera-ready copies of accepted papers are due on **March 3, 2019**. For submission details, please visit the symposium website: <http://mvl.jpnl.org/ISMVL2019/>

The symposium will be co-located with the 28<sup>th</sup> International Workshop on Post-Binary ULSI Systems and Reed-Muller 2019 Workshop. Information on accommodations and transportation to the Fredericton will be provided at a later date. This event is sponsored by the IEEE Computer Society. For additional information, please contact:

**Symposium Chair**  
**Gerhard Dueck**  
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