

PROCEEDINGS

Reed-Muller Workshop 2009



Naha Culture Center: TENBUSU

Naha, Okinawa, Japan

May 23-24, 2009

Table of Contents

Preface	iii
Session S: Opening+Special	
History of Research on Switching Theory in Japan - On the Contributions of Akira Nakashima A. Yamada	1
Session 0: Transform	
Enabling Practical Uses of Arithmetic Transforms - A Comprehensive Analysis Z. Zilic and K. Radecka	9
On a Property of Spectral Transforms C. Moraga and R. Stanković	19
Session 1: Decision Diagrams	
Recent Results on BDD-Based Large-Scale Data Processing for Knowledge Discovery S. Minato	27
Modularity in Word-level Decision Diagrams B. Alizadeh and M. Fujita	33
Remarks on the Representation of Prototype Functions in LP-classification by Walsh Decision Diagrams R. Stanković, S. Stanković and J. Astola	43
Session 2: Reversible logic 1	
Reversible Synthesis of Symmetric Functions: A Hierarchical Approach S. Ghosh, B. Bhattacharya, and S. Sen Sarma	53
Toffoli Gate Cascade Generation Using ESOP Minimization and QMDD-based Swapping J. Rice, K. Fazel, M. Thornton, and K. Kent	63
Search Space Reduction for Reversible Logic Synthesis by Evaluating Lower Bounds T. Hirayama, H. Higashiohno, and Y. Nishitani	73
Session 3: Reversible logic 2	
Synthesizing Reversible Logic: An Overview R. Wille and R. Dreschler	79
Synthesizing Reversible Circuits from Irreversible Specifications Using Reed-Muller Spectral Techniques D. Miller, G. Dueck and R. Wille	87

Adaptive Equivalence-Checking for Quantum Circuits S. Yamashita and I. Markov	97
Session 4: EXOR	
The Boolean Differential Calculus - Introduction and Examples B. Steinbach and C. Posthoff	107
On the Use of Transeunt Triangles to Synthesize Fixed-Polarity Reed-Muller Expansions of Functions J. Butler, G. Dueck, S. Yanushkevich and V. Shmerko	119
Logic Functions for Cryptography - A Tutorial J. Butler and T. Sasao	127
Author Index	137